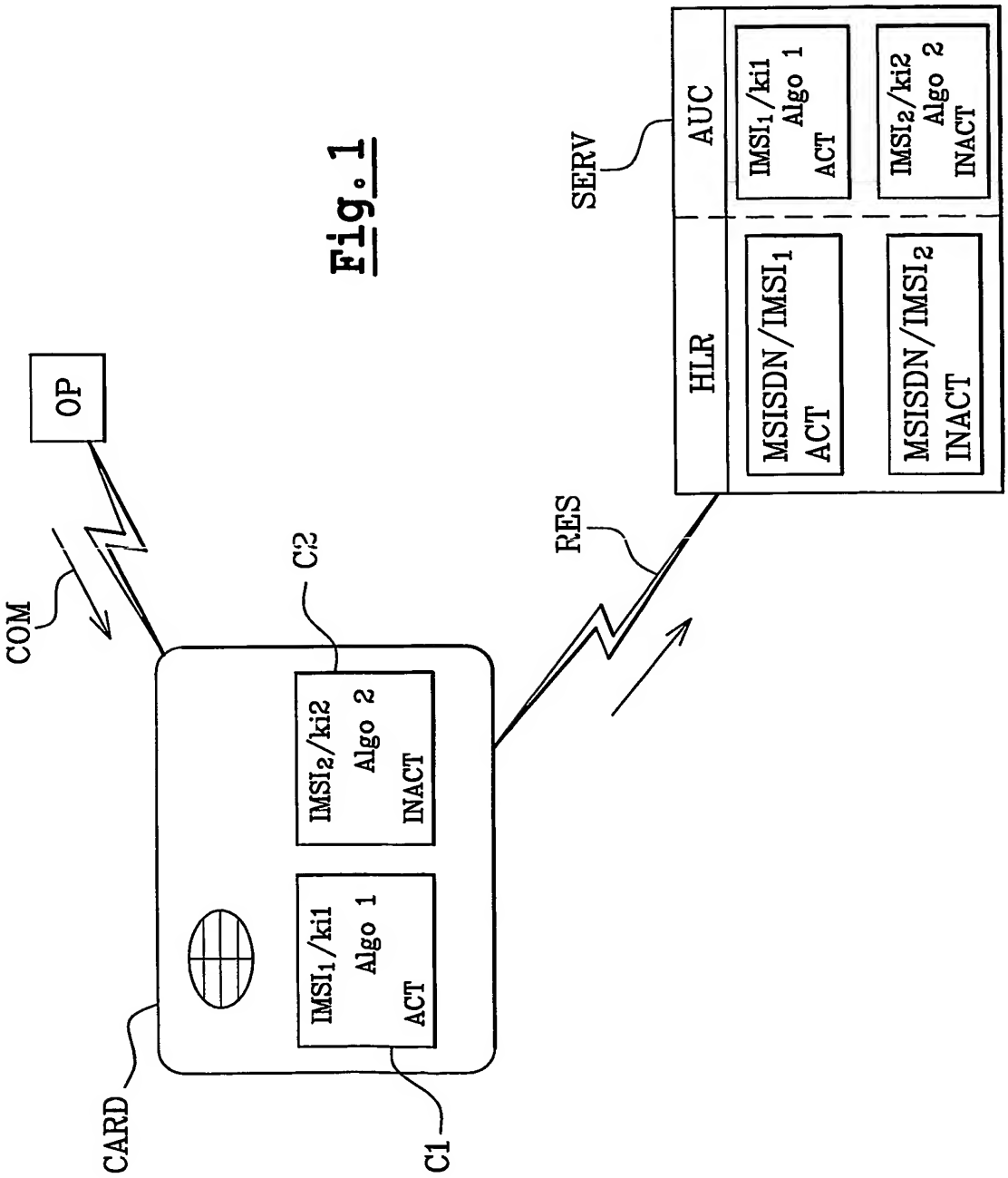
Fig. 1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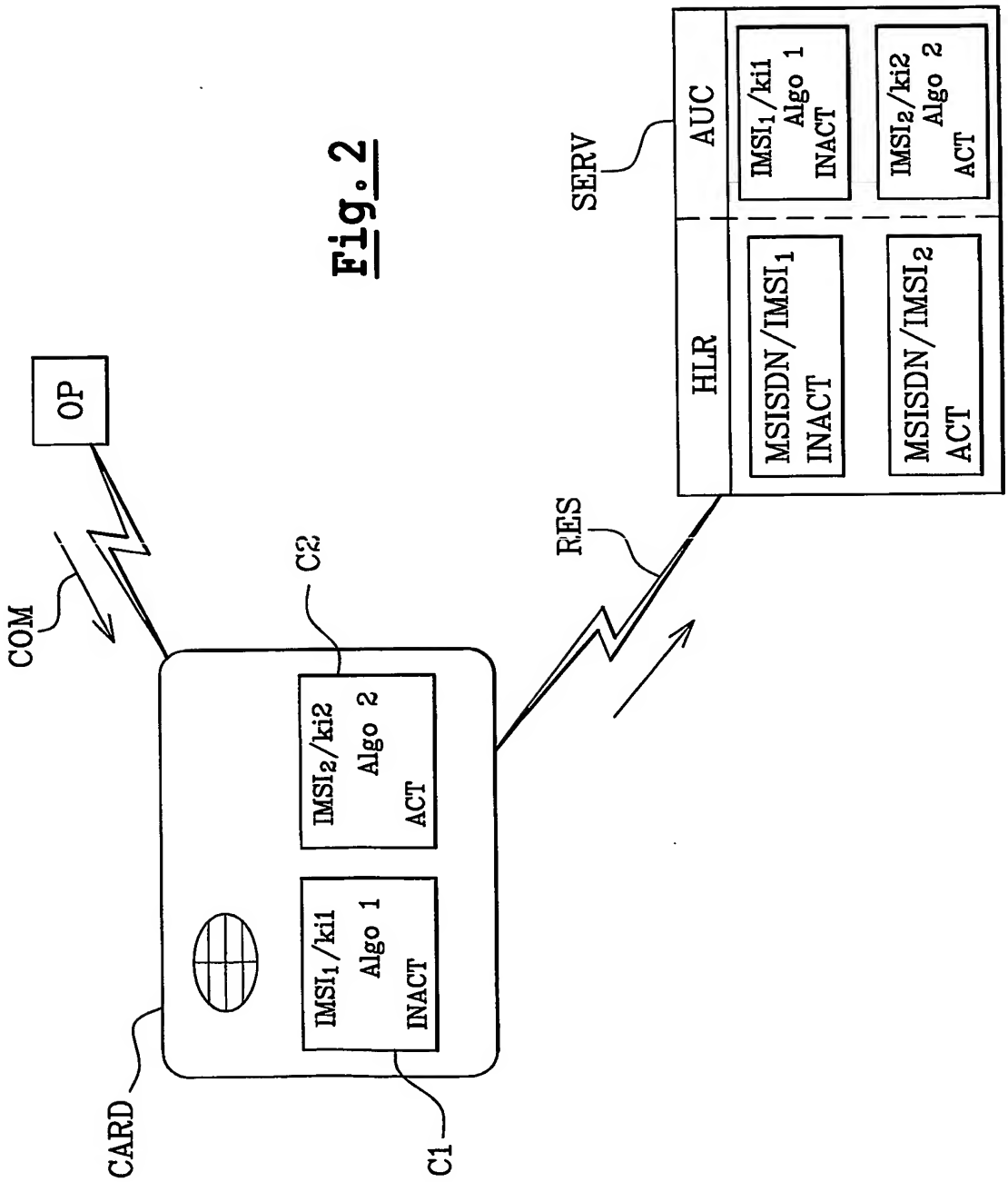
COM
C2
IMSI2/ki2
Algo 2
INACT
IMSI1/ki1
Algo 1
ACT
CARD
C1
RES
SERV
AUC
HLR
IMSI1/ki1
Algo 1
ACT
IMSI2/ki2
Algo 2
INACT
MSISDN/IMSI1
ACT
MSISDN/IMSI2
INACT

Fig. 2


OP
COM
CARD
C1
C2
IMSI2/ki2
Algo 2
ACT
IMSI1/ki1
Algo 1
INACT
RES
SERV
HLR
AUC
MSISDN/IMSI1
INACT
IMSI1/ki1
Algo 1
INACT
MSISDN/IMSI2
ACT
IMSI2/ki2
Algo 2
ACT